



**Theoretical and Practical Aspects of SPIN Model
Checking: 5th and 6th International SPIN
Workshops, Trento, Italy, July 5, 1999, Toulouse,
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Increasing the designer's confidence that a piece of software or hardware is compliant with its specification has become a key objective in the design process for software and hardware systems. Many approaches to reaching this goal have been developed, including rigorous specification, formal verification, automated validation, and testing. Finite-state model checking, as it is supported by the explicit-state model checker SPIN, is enjoying a constantly increasing popularity in automated property validation of concurrent, message based systems. SPIN has been in large parts implemented and is being maintained by Gerard Hoermann, and is freely available via ftp from netlib.bell-labs.com or from URL <http://cm.bell-labs.com/cm/cs/what/spin/Man/README.html>. The beauty of finite-state model checking lies in the possibility of building "push-button" validation tools. When the state space is finite, the state-space traversal will eventually terminate with a definite verdict on the property that is being validated. Equally helpful is the fact that in case the property is invalidated the model checker will return a counterexample, a feature that greatly facilitates fault identification. On the downside, the time it takes to obtain a verdict may be very long if the state space is large and the type of properties that can be validated is restricted to a logic of rather limited expressiveness.

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